

1

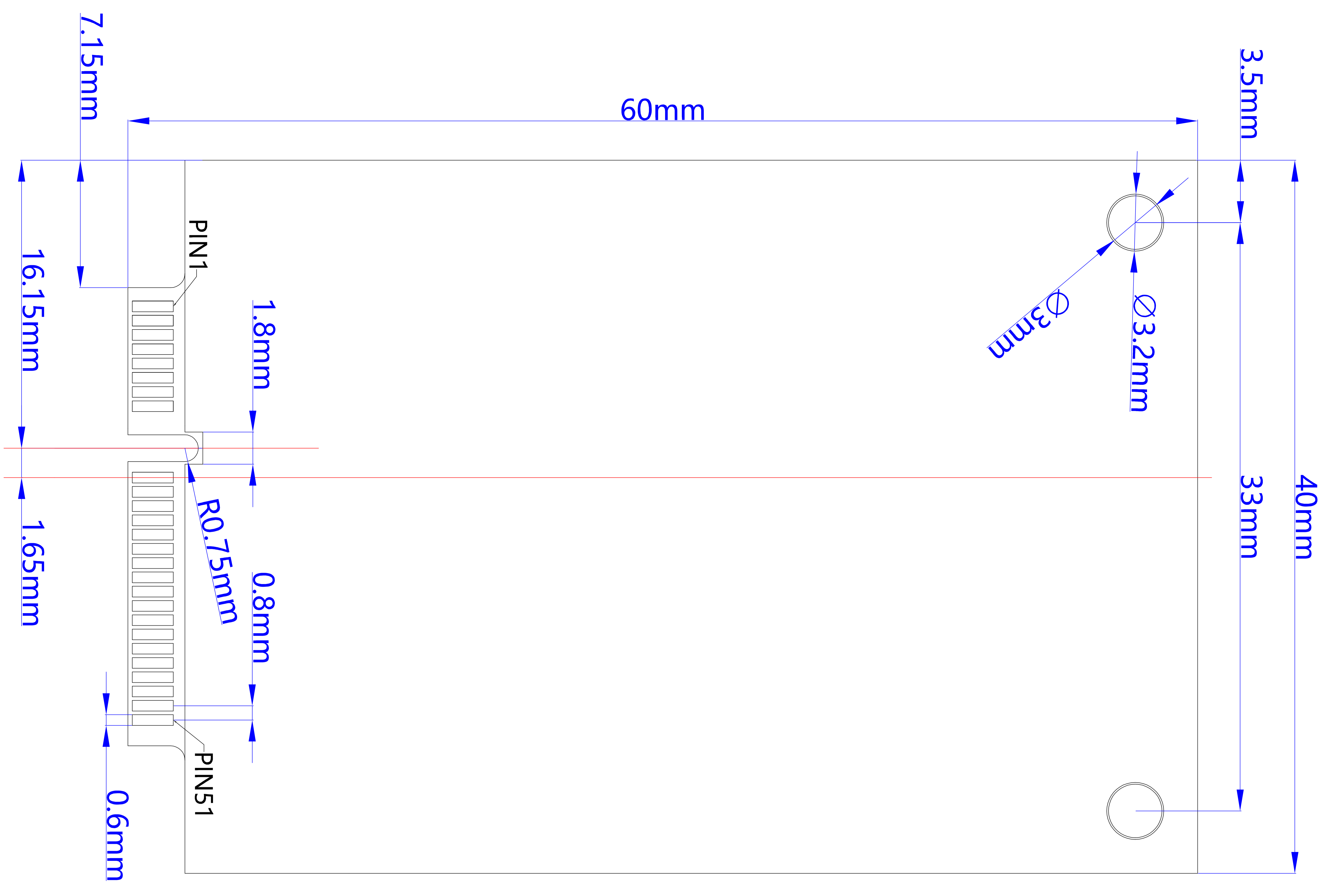
2

3

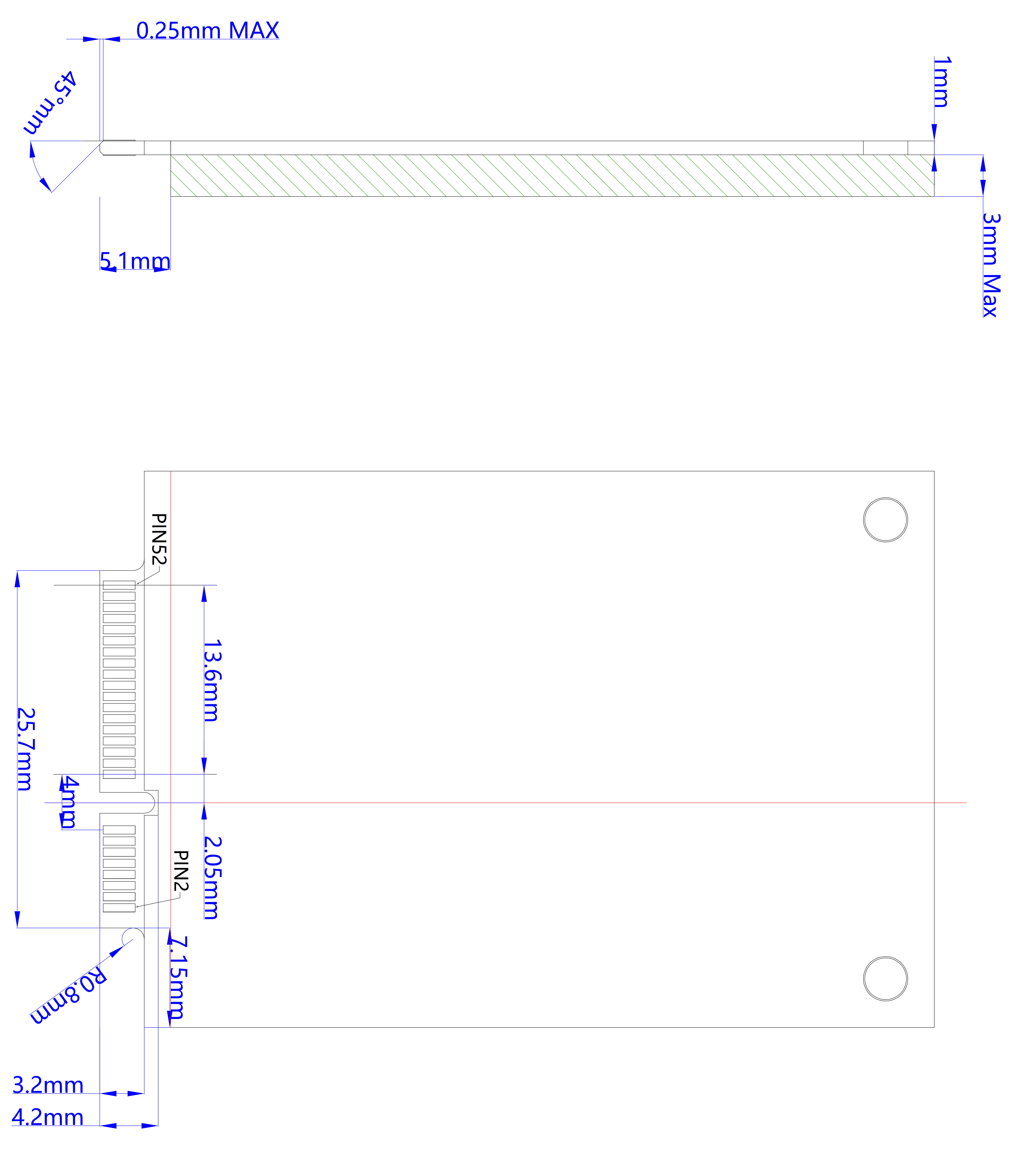
4

5

# TOP SIDE



# Bottom Side



## STEP FPGA

<b>STEP FPGA</b>			
Title	STEP_PCIE Card		
Date	2017/3/31	Version	1.0